I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: M.S. Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on:



Paul E. Rauch, Ph.D., Registration No. 38,591

Name

Our File No. CYP01-002-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Hanna A. Bamnolker, et al.)
Serial No. 09/927,863)) Examiner Lynette T. Umez Eronni
Filing Date: August 10, 2001) Group Art Unit No. 1765
For Methods of Forming Semicon- Structures Having Reduced Defects, and Articles and Dev Formed Thereby)

REQUEST FOR CORRECTION OF FILING RECEIPT

M.S. - Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Applicants have enclosed a copy of the official Filing Receipt and request the issuance of a corrected filing receipt for the above-identified application.

Applicants request the following corrections:

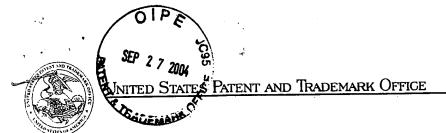
The third inventor's name is incorrect, please delete "Saurabu Dutta Chowdhury" and substitute -- Saurabh Dutta Chowdhury--.

The Commissioner is hereby authorized to charge any fees required to Deposit Account No. 50-3123. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

Paul E. Rauch, Ph.D. Registration No. 38,591

Evan Law Group LLC 566 West Adams Suite 350 Chicago, Illinois 60661 (312) 876-1400



COMMISSIONER FOR PATENTS UNITED STATES PATENT AND TRADEMARK OFFICE WASHINGTON, D.C. 20231 www.uspto.gov

TOT CLAIMS IND CLAIMS ATTY.DOCKET.NO **DRAWINGS** FIL FEE REC'D **GRP ART UNIT** FILING DATE APPLICATION NUMBER 30 4 10200/101 890 09/927,863 08/10/2001 1765

BRINKS HOFER GILSON & LIONE P.O. Box 10395 Chicago, IL 60610



CONFIRMATION NO. 7231 CORRECTED FILING RECEIPT

OC000000007420753*

Date Mailed: 02/05/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Hanna A. Bamnolker, Cupertino, CA; Chan Lon Yang, Los Gatos, CA;

Saurabh Saurabu Dutta Chowdhury, Belmont, CA; Krishnaswamy Ramkumar, San Jose, CA;

Assignment For Published Patent Application

Cypress Semiconductor Corp.;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted 09/13/2001

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

Methods of forming semiconductor structures having reduced defects, and articles and devices formed thereby